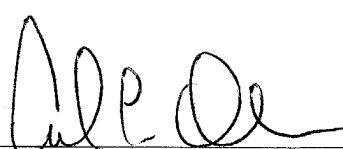



2827

<b>TRANSMITTAL OF FORMAL DRAWINGS</b>				Docket No. <b>BUR920000228US1</b>	
In Re Application Of: <b>Molinowski et al.</b>					
Serial No. <b>09/683,027</b>	Filing Date <b>11/09/2001</b>	Batch No. <b>1037</b>	Examiner	Art Unit <b>2835</b>	
Invention: <b>DUAL CHIP STACK METHOD FOR ELECTRO-STATIC DISCHARGE PROTECTION OF INTEGRATED CIRCUITS</b>					
Address to: <b>Assistant Commissioner for Patents Washington, D.C. 20231</b>					
Transmitted herewith are:  <b>9</b> sheets of formal drawing(s) for this application.  <input checked="" type="checkbox"/> Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).					
<div style="text-align: center;"> _____ <i>Signature</i></div> <div style="border: 1px solid black; padding: 5px; margin-top: 5px;"><b>Arlen L. Olsen Reg. No. 37,543 Schmeiser, Olsen &amp; Watts 3 Lear Jet Lane, Suite 201 Latham, NY 12110 (518) 220-1850</b></div>			<div style="text-align: center;">Dated: <u>2-27-2002</u></div> <div style="text-align: center; margin-top: 10px;"> _____ <i>Signature of Person Mailing Correspondence</i></div> <div style="text-align: center; margin-top: 5px;"><b>Dorothea Rubbone</b></div> <div style="text-align: center; margin-top: 5px;"><i>Typed or Printed Name of Person Mailing Correspondence</i></div>		

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I certify that this document and attached formal drawings are being deposited on 2-27-02 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.